2SK1103

Silicon N-Channel Junction FET

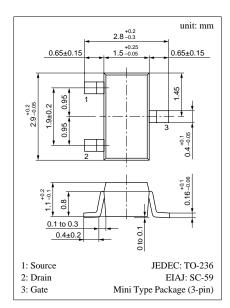
For switching Complementary to 2SJ163

■ Features

- Low ON-resistance
- Low-noise characteristics

■ Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Ratings	Unit	
Gate to Drain voltage	V _{GDS}	-65	V	
Drain current	I_{D}	20	mA	
Gate current	I_{G}	10	mA	
Allowable power dissipation	P _D	150	mW	
Channel temperature	T _{ch}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



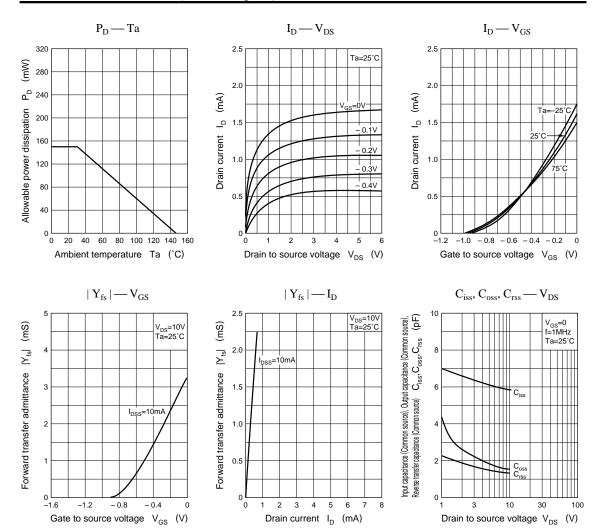
Marking Symbol (Example): 4L

■ Electrical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS} *	$V_{DS} = 10V, V_{GS} = 0$	0.2		6	mA
Gate to Source leakage current	I_{GSS}	$V_{GS} = -30V, V_{DS} = 0$			-10	nA
Gate to Drain voltage	V _{GDS}	$I_{G} = -10\mu A, V_{DS} = 0$	-65			V
Gate to Source cut-off voltage	V _{GSC}	$V_{DS} = 10V, I_{D} = 10\mu A$		-1.5	-3.5	V
Forward transfer admittance	Y _{fs}	$V_{DS} = 10V, I_D = 1mA, f = 1kHz$	1.8	2.5		mS
Drain to Source ON-resistance	R _{DS(on)}	$V_{DS} = 10 \text{mV}, V_{GS} = 0$		300		Ω
Input capacitance (Common Source)	C _{iss}	V 10V V 0 5 1MH-		7		pF
Reverse transfer capacitance (Common Source)	C _{rss}	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$		1.5		pF

 $^{^{\}ast}$ I_{DSS} rank classification

Runk	0	P	Q	R
I _{DSS} (mA)	0.2 to 1	0.6 to 1.5	1 to 3	2.5 to 6
Marking Symbol	4LO	4LP	4LQ	4LR



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